## **Lesson Plan**

Name of Faculty : Ms. Priyanka Handa

 $\begin{array}{ccc} \textbf{Discipline} & : & \textbf{ECE} \\ \textbf{Semester} & : & \textbf{3}^{rd} \ (\textbf{odd}) \end{array}$ 

Subject : Digital Electronics (PC/ECE/31-T)

Week		Theory		
	Lecture Day	Topic (Including Assignment/Test)	Date	Director- Principal
1 <sup>st</sup>	1	Digital Fundamentals: Decimal, Binary, octal ,hexadecimal		1
	2	1's and 2's complements		
	3	Binary codes: BCD, Excess-3, Gray		
2 <sup>nd</sup>	4	Alphanumeric codes, Boolean theorems		
	5	Logic Gates and Universal gates		
	6	Sum of products, product of sums, MIN TERMS,MAX TERMS		
3 <sup>rd</sup>	7	K-MAP		
	8	Quine-Mc Cluskey method of minimization		
	9	Design of half adder and full adder		
4 <sup>th</sup>	10	Combinational circuits: Half and full subtractors		
	11	Binary parallel adder, carry look ahead adder		
	12	BCD adder, code converter		
5 <sup>th</sup>	13	Multiplexer, Demultiplexer		
	14	Magnitude comparator		
	15	Decoder, Priority Encoder, Encoder		
6 <sup>th</sup>	16	Sequential Circuits: SR,JK Flip Flop		
	17	T,D FF		
	18	Master Slave FF		
7 <sup>th</sup>	19	Triggering of FF, Conversion of FF		
	20	Ripple counter		
	21	Ring counter		
8 <sup>th</sup>	22	UP down counter		
	23	Shift Register, Universal Shift Register		
	24	Memory devices: ROM,PROM		
9 <sup>th</sup>	25	EPROM,EEPROM		
	26	EAPROM		
4.045	27	RAM,Static and Dynamic RAM		
10 <sup>th</sup>	28	PLA		
	29	PAL		
	30	FPGA		
	31	Logic levels		
	32	Propagation delay		
	33	Power Dissipation		
12 <sup>th</sup>	34	Fan in,Fan out		
	35	Practice of Number systems		
13 <sup>th</sup>	36	Conversion of Flip-flop Practice ADDER/Subtarctor revision		
	37	MCQ based on Unit 1& 2		
	38 39	MCQ based on Unit 1& 2 MCQ based on Unit 1& 2		
	39	INICY DASCU OII OIIIL T& 2		