

Lesson Plan

Name of Faculty : Dr. Suman Rani, Guest Faculty of ECE
Discipline : Computer Science Engg.
Semester : 5TH (odd)
Subject : Principal of Digital Electronics (OE/ ECE/ 51-T)

Week	Theory		Topic Covered Date and Remarks		
	Lecture Day	Topic (Including Assignment/Test)	Date	HOD	Director-Principal
1 st	1	Digital Fundamentals: Decimal, Binary, octal, hexadecimal			
	2	1's and 2's complements			
	3	Binary codes: BCD, Excess-3, Gray			
2 nd	4	Alphanumeric codes, Boolean theorems			
	5	Universal gates			
	6	Sum of products, product of sums, MIN TERMS, MAX TERMS			
3 rd	7	K-MAP			
	8	Quine-Mc Cluskey method of minimization			
	9	Design of half adder and full adder			
4 th	10	Combinational circuits: Half and full subtractors			
	11	Binary parallel adder, carry look ahead adder			
	12	BCD adder,			
5 th	13	Multiplexer, Demultiplexer			
	14	Magnitude comparator			
	15	Decoder, Priority Encoder, Encoder			
6 th	16	Sequential Circuits: SR, JK Flip Flop			
	17	T, D FF			
	18	Master Slave FF			
7 th	19	Triggering of FF, Conversion of FF			
	20	Ripple counter			
	21	Ring counter			
8 th	22	UP down counter			
	23	Shift Register, Universal Shift Register			
	24	Memory devices: ROM, PROM			
9 th	25	EPROM, EEPROM			
	26	EAPROM			
	27	RAM, Static and Dynamic RAM			
10 th	28	PLA			
	29	PAL			
	30	FPGA			
11 th	31	Logic levels			
	32	Propagation delay			
	33	Power Dissipation			
12 th	34	Fan in, Fan out			
	35	Practice of Number systems			
	36	Conversion of Flip-flop Practice			
13 th	37	ADDER/Subtractor revision			
	38	MCQ based on Unit 1 & 2			
	39	MCQ based on Unit 1 & 2			