

Ch. Devi Lal State Institute of Engg. & Tech., Panniwala Mota (Sirsa)

Electrical Engineering Department

No. CDLSIET/EE/2025/ 70

Dated: 10/10/25

NOTICE

Subject: Allotment of Class Representative of Electrical Engineering Department.

It is hereby informed to all the students, staff and faculty of Electrical Engineering Department that the following students of B.Tech. (EE) are nominated as Class Representative (CR) class-wise and year-wise as per the details given below:

Sr. No.	Branch	Year	Name of Class Representative	Roll No.
1.	EE	1 st Year	Mannat	253016
2.			Daksh	253006
3.	EE	2 nd Year	Amisha	24098116800001
4.			Anuj Goyal	243902
5.	EE	3 rd Year	Rohit	23098116800007
6.			Tanu Shree Anand	24098116801024
7.	EE	4 th Year	Neelam Devi	22098116800006
8.			Lakshya Gupta	23098116801012


Head (Electrical Engg.)

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Panniwala Mota (Sirsa)

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CC:

1. PA to Director-Principal-for kind information of Director-Principal
2. Notice Board (Institute/ Department/ Hostels)
3. Programmer with a request to upload on the website