Lesson Plan

Name of faculty : Mr. Surender Chahal

Discipline : Electrical Engineering

Semester : 3rd

Subject : NAS Lab (EE-203 P)

Lesson plan duration : 15 weeks

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Week |  | Practical Covered Date | | |
|  | Lecture  Day |  | Practical  Day | Topic |
| **Unit-I** | | | | |
| 1st | 1 |  | 1 | To Study the Transient response of RC circuit |
| 2 |  |
| 3 |  |
| 4 |  |
| 2nd | 5 |  | 2 | To Study the Transient response of RL circuit |
| 6 |  |
| 7 |  |
| 8 |  |
| 3rd | 9 |  | 3 | To find the resonance frequency, Band width of RLC series circuit. |
| 10 |  |
| 11 |  |
| 12 |  |
| **Unit-II** | | | | |
| 4th | 13 |  | 4 | To calculate and verify "Z" parameters of a two port network. |
| 14 |  |
| 15 |  |
| 16 |  |
| 5th | 17 |  | 5 | To calculate and verify "Y" parameters of a two port network |
| 18 |  |
| 19 |  |
| 20 |  |
| 6th | 21 |  | 6 | To determine equivalent parameter of parallel connections of two port network. |
| 22 |  |
| 23 |  |
| 24 |  |
| **7th** | **1st Minor Test** | | | |
| **Unit-III** | | | | |
| 8th | 25 |  | 7 | Viva-Voice 1st |
| 26 |  |
| 27 |  |
| 28 |  |
| 9th | 29 |  | 8 | To plot the frequency response of low pass filter and determine half-power frequency. |
| 30 |  |
| 31 |  |
| 32 |  |
| 10th | 33 |  | 9 | To plot the frequency response of high pass filter and determine the half-  power frequency. |
| 34 |  |
| 35 |  |
| 36 |  |
| **Unit-IV** | | | | |
| 11th | 37 |  | 10 | To plot the frequency response of band-pass filter and determine the band-width |
| 38 |  |
| 39 |  |
| 40 |  |
| 12th | 41 |  | 11 | To calculate and verify "ABCD" parameters of a two port network. |
| 42 |  |
| 43 |  |
| 44 |  |
| 13th | 45 |  | 12 | To synthesize a network of a given network function and verify its response. |
| 46 |  |
| 47 |  |
| 48 |  |
| **14th** | **2nd Minor test** | | | |
| 15th | 49 |  | 13 | Viva-Voice – 2nd. |
|  | 50 |  |
|  | 51 |  |
|  | 52 |  |