**Ch. Devi Lal State Institute of Engg. & Tech., Panniwala Mota (Sirsa)**

**Electrical Engineering Department**

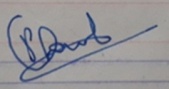
**No. CDLSIET/EE/2022/ 29 Dated: 08.03.2022**

**CIRCULAR**

**Subject: Regarding Lab Incharges, Lab Technicians and Lab Attendants in Electrical Engg Department.**

The following Faculty Members/ Staff of EE Department will work as the Lab Incharges, Lab Technicians and Lab Attendants for the respective Labs:

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **S. No.** | **Laboratory Name** | **Semester** | **Teacher Incharge (Sh./Ms.)** | **Lab Technician (Sh.)** | **Lab Attendant (Sh.)** |
| 1 | Basics of Electrical Engineering Lab | 1st / 2nd Sem. | Ruby Sathiala | Nishant Kumar | Sumit Kumar |
| 2 | Electronic Devices and Circuits Lab. | 3rd Sem. EE | Puneet Chawla | Nishant Kumar | Manjeet Kumar |
| 3 | Electrical Machines-I Lab | 3rd Sem. EE | Puneet Chawla/ Ruby Sathiala | Raj Kumar | Manjeet Kumar |
| 4 | Electrical Workshop Lab | 3rd Sem. EE | Sita Devi | Mahender Kumar | Manjeet Kumar |
| 5 | Power Electronics Lab | 4th Sem EE | Puneet Chawla | Nishant Kumar | Manjeet Kumar |
| 6 | Electrical Machines-II Lab | 4th Sem. EE | Ruby Sathiala | Raj Kumar | Sumit Kumar |
| 7 | Power Systems-I Lab | 4th Sem. EE. | Sita Devi | Mahender Kumar | Manjeet Kumar |
| 8 | Advanced Power Electronics and Drives Lab | 5th. Sem. EE. | Puneet Chawla/ Ruby Sathiala | Raj Kumar | Sumit Kumar |
| 9 | Control Systems-I Lab | 5th Sem. EE | Sita Devi | Mahender Kumar | Manjeet Kumar |
| 10 | Microprocessors & Microcontrollers Lab | 5th Sem. EE | Sita Devi | Mahender Kumar | Sumit Kumar |
| 11 | Power Systems-II Lab | 6th Sem. EE | Puneet Chawla | Nishant Kumar | Manjeet Kumar |
| 12 | Electrical Measurements & Instrumentation Lab | 6th Sem. EE | Sita Devi | Mahender Kumar | Manjeet Kumar |
| 13 | Control Systems-II Lab | 6th Sem. EE | Sita Devi | Mahender Kumar | Manjeet Kumar |
| 14 | Minor Project Lab | 7th Sem. EE | Puneet Chawla/ Ruby Sathiala | Raj Kumar | Sumit Kumar |
| 15 | Computer Methods in Power Systems Lab | 8th Sem. EE | Puneet Chawla | Nishant Kumar | Sandeep Kumar |
| 16 | Major Project Lab | 8th Sem. EE | Puneet Chawla/ Ruby Sathiala | Raj Kumar | Sumit Kumar |
| 17 | Electrical Engg.Departmental Library | - | Puneet Chawla/ Sita Devi | Nishant Kumar | Manjeet Kumar |

The above staff members will be responsible for the requirements, new purchase, maintenance nd cleanliness of the respective labs. 

Head (Electrical Engg.)

CDL State Institute of Engg. & Tech.

Panniwala Mota (Sirsa)

CC to:-

1. PA to Director-Principal- for kind information of the Director-Principal.
2. Concerned Faculty/ staff.