

# **Information for Admission in B. Tech.**

## **(Engineering) Courses**

**Ch. Devi Lal State Institute of Engineering  
& Technology, Panniwala Mota (Sirsa)**

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

**Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota was formally inaugurated on 26 September, 2003.**

**It is named after Ch. Devi Lal, the former deputy Prime Minister of India and also the former Chief Minister of Haryana.**

**The Chaudhary Devi Lal State Institute of Engg. & Tech., Panniwala Mota, Sirsa was established to facilitate and promote studies and research in emerging areas of Engineering education with focus on new frontiers of technology, and to achieve excellence in the connected fields.**

**The Institute is situated at Village Panniwala Mota, District Sirsa (Haryana). It is about 22 Kms. from Sirsa on Sirsa-Dabwali road. It is well connected by road from Delhi and Chandigarh as well as from your place.**

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Courses Offered since year 2003

- Computer Science & Engg. – 60 seats
- Electronics Communication & Engg. – 60 seats
- Electrical Engineering – 60 seats
- Mechanical Engg. – 60 seats
- Civil Engg. – 60 seats

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Eligibility for Basis of Admission in Engg. Courses

- **Minimum Academic Qualifications for B.E. /B.Tech.**

Should be a pass in 10+2 examination from recognized Board(**Central/State**) with **Physics and Mathematics** as compulsory subjects along with one of the following subjects:

(a) **Chemistry**

(b) **Bio-Technology**

(c) **Biology/ Technical Vocational subject**

Marks obtained at least 45% marks (40% in case of candidate belonging to SC category) in the above subjects taken together for B.E/B.Tech course.

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Eligibility for Basis of Admission in Engg. Courses**

- **Basis of admission for B.E./B.Tech.:**

**The Basis of admission for B.E/B.Tech. is inter-semester of JEE Main-2019 conducted by National Test Agency (NTA).**

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Admission Procedure

The Admissions in the Institute are made strictly on the basis of marks of JEE (Mains)/12<sup>th</sup> Marks by Haryana State Technical Education Society *which conducted the On-Line Off Campus Counselling for admission.*

For more information please visit the website of Haryana State Technical Education Society

[www.hstes.org.in](http://www.hstes.org.in) & [www.tehadmissions.gov.in](http://www.tehadmissions.gov.in)

- For online Admissions, Admission Brochure can be downloaded from our websites [www.hstes.org.in](http://www.hstes.org.in) or [www.techeduhry.gov.in](http://www.techeduhry.gov.in).
- This Admission Brochure is for admission to B.E./B.Tech. courses for the one Academic Year only.

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Admission Procedure

- Admissions for B.E/B.Tech. (All Categories including candidates under special Categories such as **Tuition Fee Waiver (TFW) scheme** shall be made on the basis of the inter-se-merit of JEE (Main)-2019 through On-line Off- Campus counselling at [www.techadmissionshry.gov.in](http://www.techadmissionshry.gov.in)
- Admission for B.E/B.Tech. for the candidates of special categories such as under Kashmiri Migrants (K.M.) category and under Haryana Government School Toppers (HGST) category shall be made on the basis of inter-se merit of percentage of qualifying exam through On-line Off-Campus counselling at [www.techadmissionshry.gov.in](http://www.techadmissionshry.gov.in).

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Admission Procedure

- **Application Fee of Rs. 200/- for Online Registration Fee for the participation in the Counselling.**
- **Counselling fee of Rs. 500/- for All categories of B.E./B.Tech shall be deposited by the candidate through e-Challan generated from [www.onlinetesthry.gov.in](http://www.onlinetesthry.gov.in) during filling the online application form or through Credit/Debit Card or Net-banking by using online payment gateway during filling the online application form.**

**In case of E-Challan, the candidate shall take print out of e-Challan and shall deposit on the next day of generation of the e-Challan in any branch of following designated banks**

- **Punjab National Bank**
- **Axis Bank**

**Application fee: Different for different category students**



# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Admission Procedure

- **Two Counsellings will be conducted for admission to all B.E/B.Tech for all the seats of Govt. / Govt. Aided as follows under all categories:**
- **1st Counselling** - shall be done for all categories namely AIC (ROHC), HOGC, KM,TFW, SC, BC-A, BC-B, PH, ESM (with all priorities) and FF of all categories with 25% Horizontal Female reservation.
- **2nd Counselling** - shall be done for all categories namely AIC(ROHC), HOGC, KM, TFW, SC, BC (by merging BCA and BCB in BC), PH and merging all ESM (all priorities) and FF with 25% Horizontal Female reservation.

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Eligibility for Basis of Admission in Engg. Courses

- After 2<sup>nd</sup> counselling, online Off Campus Counselling shall also be conducted by HSTES for the admission *after merging the sub-categories of a particular reserved category into the main reserved category of the State* in the event of seats remaining vacant/unfilled/ Leftover, same shall be filled up before final cut off date of admissions without any reservation, firstly on the basis of entrance exam merit and only thereafter on the basis of marks of qualifying examination **i.e. marks of class 12<sup>th</sup> .**

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Distribution of Seats of Engg. Courses

Category	Code	Percentage
All India Category Seats	AIO	15% of sanctioned intake
State Quota		85% of sanctioned intake
Haryana Open General Category (General)	HOGC	50% of State Quota
Reserved Categories of Haryana		50% of State Quota
Scheduled Castes	SC	20% of State Quota
Backward Classes (A)	BCA	16% of State Quota
Backward Classes (B)	BCB	11% of State Quota
Physically Handicapped	PH	3% of State Quota
Ex-Servicemen/ Freedom Fighters & their dependants		3% of State Quota ( 1% General, 1% SC, 1% of BC)
Kashmiri Migrants		5% (supernumeray seats)
Haryana Govt. School Toppers	HGST	1 seat for each course
Tuition Fee Waiver	TFW	5% (supernumeray seats)

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Fee Structure and Scholarship Schemes In Institute

- **Total Fee to be paid per year = Rs. 40000/- per Year including University Charges.**

At the time of Admission, Rs. 26000/- to be paid & balance in the next semester.

- *Admission Fee for SC students under PMS = Rs. 4500/- per year* (Family Income should be less than Rs. 2.5 Lac and Parents are not in Govt. Job and students must be of SC category of Haryana State)

Admission Fee for students under Tuition Fee Waiver (TFW) Scheme- Rs. 11000/- per Year (Family Income should be less than Rs. 8.0Lac)

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Scholarship Schemes implemented in Institute

Sr. No.	Name of Scholarship scheme	Stipend of Scholarship
1.	Scholarship Scheme of SC students under PMS	Rs. 35000/- per Year + Maintenance Allowance <i>(Almost complete fee is paid in Stipend by Govt.)</i>
2.	Scholarship Scheme for BC students <b>(Family Income should be less than 1.0 Lac)</b>	Rs. 3500/- per Year (for Non-Hosteller) Rs. 7200/- per Year (Hostel student)
3.	B.R. Ambedkar Merit Scholarship Scheme (For SC/ BC students) <b>This scheme is on basis of Merit of Examination.</b>	Rs. 8000-Rs. 10000/- per Year for based on Merit of Examination
4.	Scholarship Scheme for J& K Students (Prime Minister Special Scholar Scheme for J&K Students)	Rs. 40000/- per Year ( Stipend will be paid by AICTE, New Delhi)
5.	Scholarship Scheme of UP/ Bihar residents	Stipend will be paid by the respective State Govt. of UP and Bihar for their students studying in the Govt. Institution of other States.

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Computer Sc. & Engg.**

- **Computer Sc. & Engg. branch in Ch. Devi Lal State Inst. of Engg. & Tech., Panniwala Mota (Sirsa) offers B. Tech. Course since 2003 with an intake of 60 seats.**
- **The aim of the programme is to enable the students to acquire specialized knowledge for various subjects of Computer Science and Engineering as well as to enrich the student's personal, social and cognitive development to meet challenges of today and tomorrow.**
- **The department is well-equipped with the state-of-the-art laboratories of all major domains of Computer Science and Engineering.**
- **The department has well experienced and dedicated faculty with different specializations.**

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Computer Sc. & Engg.

- The Computer Sc. & Engg. course includes: System Programming & System Administration, Web development through ASP, PHP, JSP, HTML, JAVA, CSS, Java Programming, C/C++ Programming, Compiler Design, Automata Theory, Intelligence System, Wireless Communication, Digital System Design, Security Information System, Web Engineering, Distributed Operating System, Software Project Management and many more.

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Career Prospectus in Computer Sc. & Engg.**

- **Software Developer**
  - **Software tester**
  - **Network Administrator**
  - **Database Administrator**
  - **Database Operator**
  - **Website Designer**
  - **Website developer**
  - **Mobile application developer**
  - **Robotics expert**
  - **Software analyst**
  - **Mobile expert**
  - **Hardware engineer**
  - **Data Scientist**
  - **Business Intelligence analyst**
  - **Database developer**
  - **Data security administrator**
  - **Manufacture and Maintenance**
  - **Programmer**
  - **Software support analyst**
  - **Teacher/ Faculty**
  - **Higher Studies**
- (M. Tech., M.S., Ph.D. etc.)**



# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Electronics and Communication Engg.**

- **In the last 50 years, the field of Electronics has grown so rapidly that it has affected every walk of life.**
- **Hence, to impart the basic as well advance knowledge, the emphasis is laid on strong foundation of theory as well as practicals. So, keep in touch with the pace of Electronics, Electronics and Communication Engg. branch was established in CDLSIET Panniwala Mota since 2003 with an intake of 60 seats.**
- **Apart from strong foundation of Basic Electronics , the students are provided with in depth Knowledge of Assembly Language Programming, Digital Electronics, Digital & Analog Communication, Electromagnetic, Network systems, Antenna & Microwave, Signals and Systems, Electronic Measurement and Instrumentation.**

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Electronics and Communication Engg.**

- **Moreover students have to undergo extensive training in Microprocessor Programming, Circuit Design, Simulation tools, Software like MATLAB, ORCAD, PULSONIX, Emerging Technologies like DSP, System Design & Embedded System Design, VLSI Design are covered thoroughly.**
- **Students are also encouraged to take part in Technical Paper Presentations, and also to undertake live Projects from Industry.**

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Career Prospectus in Electronics and Communication Engg.

- Industry deal with Computers and Electronics,
- Huge scope for Radio and Television Industry
- Jobs with Private and Public Telecommunication systems
- Software Engineers interested with Electronics and Communication Engg. Background
- Jobs in Power Sector, Steel Plants & Chemical Industry.
- Industry in Public and Private Industry with regards to Installation, Operation and Maintenance of electronics equipments & systems.
- Design, fabrication and manufacturing of electronics equipments
- Teaching and research areas in India and abroad.

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Electrical Engg.

- The whole world now depends upon electricity for almost all the processes and activities. No instrument can run without electricity and the usage of power is increasing day- by- day. So, Electrical Engineering will never face a decline in job market. Employment opportunities are endless; one is to keep a track of latest inventions and innovations for developing their skills.
- So, Electrical Engineering department is one of the major departments, established since 2003 with an intake of 60 seats with 20% LEET seats for deserving candidates across the country,
- With the Institute, CDLSIET Panniwala Mota, being raised, the department has been established various laboratories with a large no. of equipments and kits & machines in various course curriculum and emerging areas of Electrical Engg.

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Electrical Engg.

- The department faculty is highly qualified and experienced with actively participation in research, projects, industrial training and visits to explore the Electrical Engg. field as per today's trends and need of the Society.
- Students are developing their ability through various opportunities in the areas like Power Systems, Control System, Electric Machines, Electric Power Generation and its effect use, Power Electronics & Drives etc. and serve their professional & civic communities with improvement in their overall ethical and spiritual values.

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Nomenclature of Laboratory already established in Electrical Engg. department

Sr. No.	Nomenclature of Laboratory established in Electrical Engg. department	Class & Semester
1.	Electrical Technology Lab	1st Year
2.	Electrical Measurements & Measuring Instruments Lab	3 <sup>rd</sup> Sem. EE
3.	Electrical Machines-I Lab	3 <sup>rd</sup> Sem. EE
4.	Network Analysis and Synthesis Lab	3 <sup>rd</sup> Sem. EE
5.	Electrical Workshop Lab	3 <sup>rd</sup> Sem. EE
6.	Skills & Innovation Lab	3 <sup>rd</sup> Sem. EE
7.	Electrical Machines-II Lab	4 <sup>th</sup> Sem. EE
8.	Digital Electronics Lab	4 <sup>th</sup> Sem. EE
9.	Electronics Measurement & Process Control	4 <sup>th</sup> Sem. Food Tech.
10.	Power System Lab	4 <sup>th</sup> Sem. & 7 <sup>th</sup> Sem. EE

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Nomenclature of Laboratory established in Electrical Engg. department

Sr. No.	Nomenclature of Laboratory established in Electrical Engg. department	Class & Semester
11.	Power Electronics-I & II Lab	5 <sup>th</sup> Sem. & 6 <sup>th</sup> Sem. EE
12.	Electric Drives Lab	6 <sup>th</sup> Sem. EE
13.	Electronic Measurement Lab	5 <sup>th</sup> Sem. EE
14.	Control System Lab	5 <sup>th</sup> Sem. EE
15.	Microcontroller Lab	6 <sup>th</sup> Sem. EE
16.	Transducers Applications Lab	7 <sup>th</sup> Sem. EE
17.	Minor Project Lab.	7 <sup>th</sup> Sem. EE
18.	Computer Methods in Power System Lab.	8 <sup>th</sup> Sem. EE
19.	Electrical Engg. Departmental Library	-

- **For complete details of Lab equipments, please visit Institute website,**

**[www.cdlsiet.ac.in](http://www.cdlsiet.ac.in)**

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Career Prospectus in Electrical Engg.**

- **Central Electricity Agencies/ Corporations such as NTPC, NHPC, Ministry of Power, Central Electricity Commission, Power Grid Corporation of India, Oil Corporation (Indian Oil, Hindustan Petroleum, BPCL, ONGC, GAIL, SAIL etc.**
- **State Electricity Boards/ Corporations such as Haryana Power Generation Corporation Ltd., HVPNL, UHBVN, DHBVN, State Load Dispatch Centre (SLDC) and other state owned/ funded organizations.**
- **Indian Engg. Service (IES), Indian Admin. Service (IAS), Police Services etc., Defense Research Development Organization (DRDO), Indian Space Research Organization (ISRO)**
- **Indian Defense Sectors (Indian Army/ Indian Navy/ Indian Air Force)**
- **Indian Railways**
- **Jobs in the sector of Power Generation from Solar, Wind, Biogas/ Biomass, Fuel Cell, from Waste etc.**



# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Career Prospectus in Electrical Engg.**

- **Design Engineer, Manufacturing Engineer, Installation Engineer, Maintenance Engineer**
- **Heavy Electrical Manufacturing Industries such as Bharat Heavy Electrical Limited (BHEL), Crompton Greeves, ABB, Siemens, Havells, Syska, Crompton, Kilsokar Motors & Pumps etc. and many more**
- **Jobs in the sector combined with Computer Sc. & Engg. and Electronics Engg. & Service Sectors**
- **Jobs in the sector of Hybrid Electric Generation**
- **Jobs in the sector of Electric vehicle and transportation**
- **Electrical Engineering Scientist, Research Scientist**
- **Entrepreneurs in field of Substation Installation, Transformer Manufacturing, Renewable Energy Resources etc.**

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Mechanical Engg.**

- **Mechanical Engineering Department of CDLSIET Panniwala Mota is well equipped in the conventional areas of material testing with sophisticated equipments dealing with Production engineering., Thermal engineering., Fluid Mechanics etc. since 2004 with an intake of 60 seats.**
- **It has well equipped labs in the field of mechanics of Machines, hydro and aero machinery, internal combustion engines, refrigeration and air conditioning, vibration, heat transfer etc.**
- **The Deptt. has well devoted and qualified expert team of Faculty, who are constantly engaged in carrying out various live projects for the students and also the Deptt. has sufficient infrastructure to support the same. In general, practically experience is considered to be very important part of the Mechanical Engineering Programme.**

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Career Prospectus in Mechanical Engg.**

- **Central Electricity Agencies/ Corporations such as NTPC, NHPC, Ministry of Power, Central Electricity Commission, Power Grid Corporation of India, Oil Corporation (Indian Oil, Hindustan Petroleum, BPCL, ONGC, GAIL, SAIL etc.**
- **State Electricity Boards/ Corporations such as Haryana Power Generation Corporation Ltd., HVPNL, UHBVN, DHBVN, State Load Dispatch Centre (SLDC) and other state owned/ funded organizations.**
- **Indian Engg. Service (IES), Indian Admin. Service (IAS), Police Services etc., Defense Research Development Organization (DRDO), Indian Space Research Organization (ISRO)**
- **Indian Defense Sectors (Indian Army/ Indian Navy/ Indian Air Force)**
- **Indian Railways**
- **Analyze problems, design or redesign of mechanical and thermal devices and using computer methods**

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Career Prospectus in Mechanical Engg.**

- Mechanical engineers design power-producing machines, such as electric generators, internal combustion engines, and steam and gas turbines, as well as power-using machines, such as refrigeration and air-conditioning systems.
- Mechanical engineers design other machines inside buildings, such as elevators and escalators. They also design material-handling systems, such as conveyor systems and automated transfer stations.

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **Civil Engg.**

- **India is on its way to become one amongst the developed nations in the world. In this process the infrastructure development is in full swing. Roads, buildings, Railways, Airways, space programs are all progressing at fast pace. Civil Engineering has a key role to play in this developmental activity and therefore, there are enormous opportunities lying ahead for civil engineers. Civil Engineering Department of the Institute was established since 2006 with an intake of 30 seats and in the year 2009, seats were increased to 60.**
- **The courses include Structural Analysis, Surveying, Concrete Structures, Steel Structures, Geotechnology, Highway and Transportation, Environmental Engg., Fluid Mechanics & Computer applications.**
- **The civil engineering department is started with the objective of producing able and young civil engineers to aid in this infrastructure development**

# Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)

## Career Prospectus in Civil Engg.

- **Jobs as Executive, Assistant and Junior Engineers in different Government departments like PWD, Public Health, Irrigation etc.**
- Site engineers for different Multi-National/ National level constructions companies
- **Design Engineer.**
- Project Managers
- **Entrepreneurs**
- Higher studies

After doing Engineering courses, one can also get jobs in non-IT companies such as Universities, Academic Institutions, research, all types of private and public industries, government departments, business organization, commercial organization and many more.

# **Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)**

## **OUR VISION**

**WE ALL SHOULD WORK HARD FOR THE GLORY  
AND DIGNITY OF THIS INSTITUTION**