

**CH. DEVILAL STATE INSTITUTE OF ENGINEERING AND TECHNOLOGY
PANNIWALA MOTA (SIRSA), HARYANA, INDIA -125077**

(Department: Computer Science and Engineering)

No: CDLSIET/CSSE/2020/13/641

Date: 17/11/2020

NOTICE

(Revised and Extra Lecture Time Table for LEET Students CSE 3rd Semester of odd Semester)

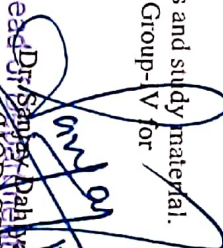
Period No. & Time	1	2	3	4	5	6	7	8	
Day ↓	9.00-9.55 AM	9.55-10.50 AM	10.50-11.45 AM	11.45-12.40 PM	12.40-1.20 PM	1.20-2.15 PM	2.15-3.10 PM	3.10-4.05 PM	4.05-5.00 PM
Monday	E.V.S.	Data Structures and Algorithms-PCC-CSE 201-T	Math-III	OOPS	L	*COA	*OOPS	*Math-III	*E.V.S.
Tuesday	*Discrete Structure	Data Structures and Algorithms PCC-CSE 201-T	Math-III	OOPS	U	Data Structures and Algorithms using C/C++ GP-IV (PCC-CSE 201-P) (Lab-101)			
Wednesday	Discrete Structure	Data Structures and Algorithms PCC-CSE 201-T	COA	OOPS	N	Data Structures and Algorithms using C/C++ GP-I (PCC-CSE 201-P) (Lab-101)			
Thursday	COA	Maths-III	E.V.S.	OOPS	C	OOPS Lab GP-II			
Friday	COA	Discrete Structure	E.V.S.	*Data Structures and Algorithms PCC-CSE 201-T	H	Data Structures and Algorithms using C/C++ GP-III (PCC-CSE 201-P) (Lab-101)			
						OOPS Lab GP-IV			

Note: 1 * Extra Classes for LEET Admitted students.

- Faculty can take extra class on Saturday/Sunday without any Extra financial remuneration/benefits.
- Faculty and students may interact over Phone/WhatsApp Group/E-mail/Google Classroom/G-meet etc. for online classes and study material.
- LEET admitted students join the online classes immediately after getting the admission in the Institute and all included in Group-V for Practical/ Tutorial (if any) classes.
- Students may contact with concerned Faculty and HOD (CSE) for any discrepancies and Sessional/ Minor test.

Copy is forwarded to the following for Information and necessary action, Please.

- Director-Principal, CDLSIET Panniwala Mota.
- All Concerned Faculty of CSE, CDLSIET Panniwala Mota.
- Notice Board in the Institute.
- Notice Board in Boys and Girls Hostel.
- Programmer: For upload on Institute Website.


 Head Dr. Sanjay Dahiya
 (HOD/CSE)
 Computer Science & Engg.
 CDL State Inst. of Engg. & Tech
 Panniwala Mota (Sirsa) Hry