

Name : Poonam
 Designation : Assistant Professor
 Department : Electronics and communication Engineering
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Work experience:

FROM	TO	DESIGNATION	ORGANISATION
14 AUG 2014	Till Date	Assistant professor	CDLSIET,PANNIWALA MOTA

Educational qualifications

Qualification	Institution / University	Percentage (%)	Year
M.TECH (Electronics and communication)	JCDM college of engg.,Sirsa/GJU	CGPA-7.18	2013
B.TECH(Electronics and Communication)	JCDM College of engg,sirsa/KUK	75.4	2011
Senior Secondary (12 th)	D.A.V public school,Mandi Dabwali	80.1	2007
Secondary (10 th)	CMSS School, Mandi Dabwali	71	2005

Academic Contribution through organizing conferences/seminars/ workshops/symposia/orientation /refresher courses:

Sr. no.	Coordinator/Convener/Secretary/Member of Organizing/Advisory Committee	Title of National Conference	Organizing Institute	Duration
1.	Coordinator	RAETE-2012	JCDM	2 DAYS

Teaching Engagements

TITLE	CLASS NAME	SEMESTER
Network analysis and synthesis	B.TECH(ECE)	3 rd
Microwave Engg	B.TECH(ECE)	5th
Digital Electronics	B.TECH(CSE)	5th
Basics of electronics	B.TECH(ME)	3 RD

Engagements other than Teaching:".

- Member of “Centralized Admission Help Desk Committee”.
- Assigned Duties of “Committee Members, Admission Cell”.
- Performed “Invigilation Duties” on Offline University Examination.

Publications:

Paper Published in International Journal

- 1) Paper on “**Analyzing Delay and Capacity in Passive Optical Network**” International Journal of Scientific Engineering and Research(IJSER), Vol.1,issue1, September 2013.ISSN (Online): 2347-3878 Paper ID: 01130913.
- 2) Paper on “**Investigation of delay in passive optical network**”International journal of Technical Research,vol.2,issue 2, Aug. 2013.ISSN 2278-5787. By OITM Hisar.

Paper Published in National Journal/Conference

- 1) Review Paper on “**Positioning of Mobile system in telecommunication network**” proceedings of national conference of RAETE-2012 at JCDDM college of engg. Sirsa.
- 2) Review Paper on “**A review article on electronic waste management**” proceedings of national conference of CCEP-2012 at JCDDM college of engg. Sirs

Any Other Information

Worked on Matlab and Electronic circuit simulation software



Ms. Poonam is working as Assistant Professor in Department (Electronics and communication Engineering) at Ch. Devi Lal State Institute of Engineering and Technology Panniwala Mota (Sirsa) - Haryana, India- 125077. She is M.Tech (2013) in Electronics and communication from JCDDM Sirsa. She is having Research Papers publications in international/national journals and conferences .She has 8 year Teaching, Research and administrative Experience. She is working as Members of Various administrative and others Committee. She may be contacted through gl.poonammehta@gmail.com or 9466731897