Ch. Devi Lal State Institute of Engg. & Tech., Panniwala Mota (Sirsa)

Electrical Engineering Department

No. CDLSIET/EE/2025/ 2 //

Dated:

18/3/25

INVITATION TO PARENT-TEACHER MEETING

Dear Parents/ Guardian,

We are pleased to inform you that a Parent-Teacher Meeting (PTM) will be held on dated 25.03.2025 at 10:00AM onwards in Electrical Engineering Department of our esteemed Institution. The purpose of this meeting is to discuss the academic progress, behaviour and overall development of your ward.

We, hereby, request you to spare your valuable time and your presence to engage in an effective discussion about your ward's performance and ways we can work together to ensure their continued success.

Meeting Objectives: -

- 1. Review of student's academic performance and progress.
- 2. Indentifying areas for improvement and providing feedback.
- Addressing any apprehensions or queries to your ward's studies and well-being. 3.
- Sharing updates about upcoimg events and activities to be organized by Department/ Institute. 4.

We look forward to your valuable presence and participation in helping us support your ward's academic journey.

Warm Regards,

Head (Electrical Engg.)

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CC:

1. PA to Director-Principal-for kind information of Director-Principal

2. All Faculty members of EE department

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3. All Notice Boards (Institute/Boys Hostel/ Girls Hostel)

4. Programmer with a request to upload on the college website.