

Ch. Devi Lal State Institute of Engg. & Tech., Panniwala Mota (Sirsa)

Electrical Engineering Department

No. CDLSIET/EE/2026/ 31

Dated: 27/5/26

NOTICE

Subject: Regarding participation in the "Earn-While-You-Learn (EWYL)" Scheme implemented in the Institute w.e.f. July, 2026.

On the subject cited above, all the students of Electrical Engineering 6th semester are hereby informed that the "Earn-While-You-Learn (EWYL)" Scheme of Directorate of Technical Education, Haryana has been implemented in the Institute w.e.f. July, 2026 for the final year students of upcoming session 2026-2027.

This scheme aims to provide students with real-time practical exposure, inculcate with ethics, support them financially, and simultaneously address manpower shortages in non-teaching areas. The initiative aligns with NEP-2020, which emphasizes experimental learning, industry relevance, skill enhancement, and equitable opportunities.

Objective of the scheme:-

- To utilize students as a resource in institutional operations.
- To provide hands-on job experience enhancing employability
- To instill dignity of labour and work ethics.
- To engage students in meaningful activities within the Institute.
- To support financially weaker students.
- To improve institutional functioning by optimizing human resource.
- To reduce workload on understaffed non-teaching departments.
- To improve utilization of Centres of Excellence (CoE)
- To integrate experimental and work-based learning as per NEP-2020.

Scope of Implementation: The scheme shall be implemented in a phased manner. Initially, at least 20% of the students from total strength of the Institute, branch-wise, may be engaged under the scheme, with scope for enhancement based on performance and requirement.

Nature of Work (Illustrative):

A. Laboratories and Workshops

- Equipment handling, maintenance and upkeep.
- Assistance in lab setups and conduct of practicals.
- Minor repair work (Electrical, electronics, plumbing, furniture, CCTV)
- Deployment of DLMT students in laboratory activities.
- Website development, software updates, system maintenance.
- Engagement students for architectural/ civil surveys, measurements, drafting, or preparation of building / repair plans of Government building, under proper supervision.

B. Library

- Cataloging and arranging books.
- Issue/ return support after regular hours.
- Register and record maintenance

C. Office and Administration

- Data entry, file and inventory maintenance.
- Student attendance, hostel, and examination record preparation.
- Support in computer, library, administrative and photography- related tasks.

Working Hours and Remuneration:

- Maximum 10 hours per week.
- Rs. 100/- per hour (subject to monthly ceiling of Rs. 4000/-)
- Payment issued on verified attendance and work quality.

Selection procedure

- Applications to be invited at the start of academic year from the Final year students.
- Selection criteria:
 - Academic merit
 - Co-Curricular involvement
 - Technical/ computer skills.
- Department to allocate work based on need assessment.

So, in this matter, all students of Electrical Engineering (6th semester) are hereby informed that they can apply for the "Earn-While-You-Learn (EWYL)" Scheme in the prescribed Application Form upto 15.06.2026, alongwith the required documents, for the following posts.

(P. Anand) 27/5/26
Head (Electrical Engg.)
CDL State Institute of
Engg. & Tech
Panniwala Mota (Sirsa)

Sr. No.	Nature of Work	No. of students required	Justification / Remarks
1.	Equipment handling, maintenance and upkeep	01	Approximate 18 no. of laboratories are set up in EE department and equipment/ devices/ setup/ material worth Rs. 18,59,670/- has been purchased from 2003 to till date and in order to upkeep and maintain the lab infrastructure of EE department.
2.	Assistance in lab setup and conduct of practicals	02	For performing the various practical during lab classes in 1st Year, 2nd year and 3rd year. 01 for assistance in conducting practicals in BEE (1 st year), Electrical Machines-I & II lab, Electronic Devices & Circuit lab (Analog & Digital Circuits), Power Electronics lab 01 for Advanced Power Electronics & Drives Lab, Control Systems-I and II lab, Power Systems-I and II lab.
3.	Support in Computer, administrative and photography related works	01	For administrative work of Electrical Engineering department, record of office work, preparation of purchase file of lab equipments, preparation of reports of various activities carried out by EE department self funded as well as sponsored & funded by outside agencies such as DST Haryana, AICTE New Delhi, NITTTR Chandigarh, C-DAC etc. Capturing of various photographs, recording of videos of various activities/ session organized by EE department under ऊर्जापुनरावर्तन – The Energy Conservation Club of EE department such as seminars, FDP, STC, Quiz competitions, Writing competition, Entrepreneurship activities etc.
4.	Minor Repair Work (Electrical, CCTV Camera) For Electricity Maintenance Section of the Institute	02	In order to look after the work of electricity repair and maintenance of various buildings of the Institute such as Academic Block, Mechanical Workshop block, Street Lighting system, Boys Hostel, Girls Hostel, Residences, Sewage Treatment Plant, Water Works etc., as well as repair and maintenance of ACs, Water Coolers, CCTV cameras, there is only 01 Electrician working on HKRNL and 01 Electrician in working as Apprentice under NAPS Scheme. The findings given in Energy Audit report are also to be taken care and intime execution on the submissions of EA report is also needed So, in order to carry out the repair work smoothly, more manpower is required for the above said works & students under Earn-Way-to-Learn (EWYL) Scheme may be engaged for this purpose.

The above seats are tentative, may be changed according to the requirement, in future. The Application performa and after duly filled, submission of the Application Performa will be done in the office of the Undersigned, upto 15.06.2026.


Head (Electrical Engg.) & Officer-in-Charge (Electricity)
CDL State Institute of Engg. & Tech
Panniwala Mota (Sirsa)

Head (Electrical Engg.)
CDL State Institute of
Engg. & Tech
Panniwala Mota (Sirsa)

CC:

1. PA to Director-Principal-for kind information of Director-Principal
2. Notice Boards (College/ Department/ Hostels)
3. Programmer with a request to upload on the Institute's website.